

ACKNOWLEDGEMENT RECEIPT

Electronic Version

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Title of Invention

REDUCED DIELECTRIC CONSTANT SPACER MATERIALS INTEGRATION FOR HIGH SPEED LOGIC GATES

Submission Type : Utility Patent Filing

Application Number:

10/709652



EFS ID:

61275

Server Response:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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IGEN77	Filename= N/A BusinessRule= The access code is not included which allows for automated processing to the deposit account. #Supporting Msg:Either <us-access-code> not found in fee-xml OR actual access code not provided in the function call.

First Named Applicant: Michael Belyansky

Attorney Docket Number: FIS920040047US1

Timestamp: 2004-05-20 09:50:55 EDT

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us-ids	FIS920040047US1-usidst.xml	1874	2004-05-20
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us-request	FIS920040047US1-usrequ.xml	4394	2004-05-20
us-request	us-request.dtd	19064	2004-05-20
us-request	us-request.xsl	33300	2004-05-20
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us-assignment	FIS920040047US1Assignment2.tif	10506	2004-05-20
us-fee-sheet	FIS920040047US1-usfees.xml	2106	2004-05-20
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application-body	FIS920040047US1-trans.xml	43177	2004-05-20
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application-body	application-body.dtd	49498	2004-05-20
application-body	wipo.ent	4956	2004-05-20
application-body	mathml2.dtd	54588	2004-05-20
application-body	mathml2-qname-1.mod	13225	2004-05-20
application-body	isoamsa.ent	5191	2004-05-20
application-body	isoamsb.ent	3988	2004-05-20

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application-body	isolat1.ent	5282	2004-05-20
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application-body	isopub.ent	6621	2004-05-20
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us-declaration	FIS920040047US1Declaration3.tif	17629	2004-05-20
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us-declaration	FIS920040047US1Declaration2.tif	55944	2004-05-20
package-data	FIS920040047US1-pkda.xml	9309	2004-05-20
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